

General Description

FSMOS[®] MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The low V_{th} series is specially optimized for synchronous rectification systems with low driving voltage

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

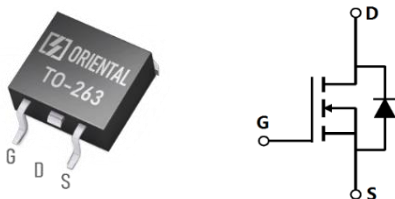
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	60	V
$I_{D, pulse}$	600	A
$R_{DS(ON), max} @ V_{GS}=10V$	2.7	$m\Omega$
Q_g	83.5	nC
PD	105	W

Marking Information

Product Name	Package	Marking
SFS06R024KF	TO263	SFS06R024K

Package & Pin information



Absolute Maximum Ratings at $T_j=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	60	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_D	150	A
Pulsed drain current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{D, pulse}$	600	A
Continuous diode forward current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_S	150	A
Diode pulsed current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{S, pulse}$	600	A
Power dissipation ³⁾ , $T_C=25^{\circ}\text{C}$	P_D	105	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	588	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.2	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	60			V	$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	1		2.5	V	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		2.4	2.7	$\text{m}\Omega$	$V_{GS}=10\text{ V}, I_D=50\text{ A}$
Drain-source on-state resistance	$R_{DS(ON)}$		3.2	3.4	$\text{m}\Omega$	$V_{GS}=4.5\text{ V}, I_D=50\text{ A}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=20\text{ V}$
				-100		$V_{GS}=-20\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=60\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	R_G		2.1		Ω	$f=1\text{ MHz}, \text{Open drain}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		5137		pF	$V_{GS}=0\text{ V}$, $V_{DS}=30\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oss}		1099		pF	
Reverse transfer capacitance	C_{rss}		48.9		pF	
Turn-on delay time	$t_{d(on)}$		15		ns	$V_{GS}=10\text{ V}$, $V_{DS}=30\text{ V}$, $R_G=2\ \Omega$, $I_D=50\text{ A}$
Rise time	t_r		10		ns	
Turn-off delay time	$t_{d(off)}$		50		ns	
Fall time	t_f		14		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		83.5		nC	$V_{GS}=10\text{ V}$, $V_{DS}=30\text{ V}$, $I_D=50\text{ A}$
Gate-source charge	Q_{gs}		14		nC	
Gate-drain charge	Q_{gd}		15.6		nC	
Gate plateau voltage	$V_{plateau}$		3.2		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	$I_S=50\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		64		ns	$V_R=30\text{ V}$, $I_S=50\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		116		nC	
Peak reverse recovery current	I_{rrm}		3.3		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_a=25\text{ }^\circ\text{C}$.
- 5) $V_{DD}=50\text{ V}$, $V_{GS}=10\text{ V}$, $L=0.3\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

Electrical Characteristics Diagrams

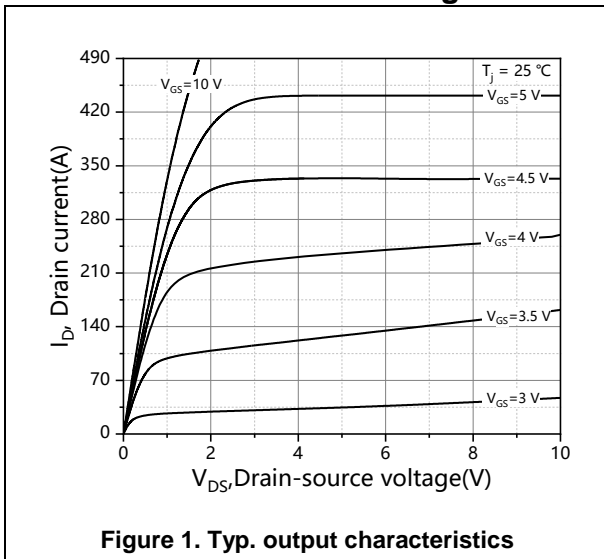


Figure 1. Typ. output characteristics

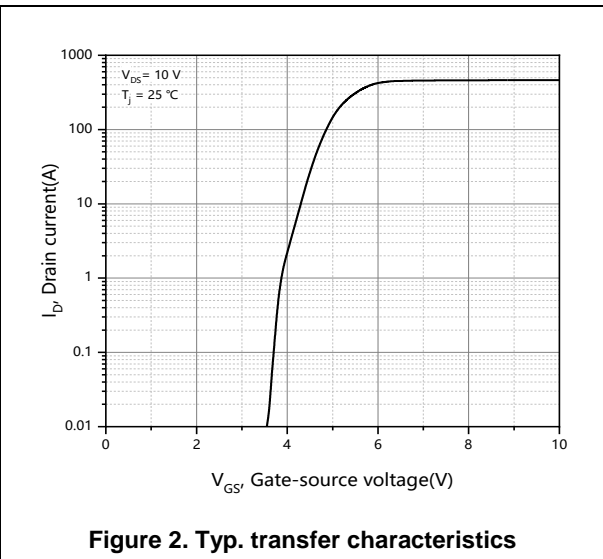


Figure 2. Typ. transfer characteristics

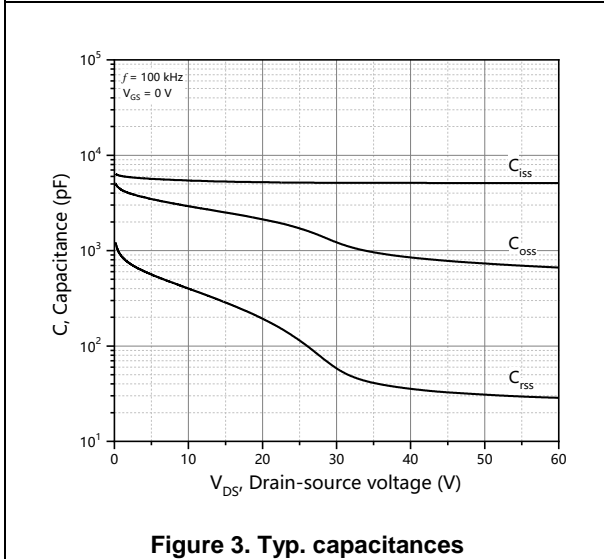


Figure 3. Typ. capacitances

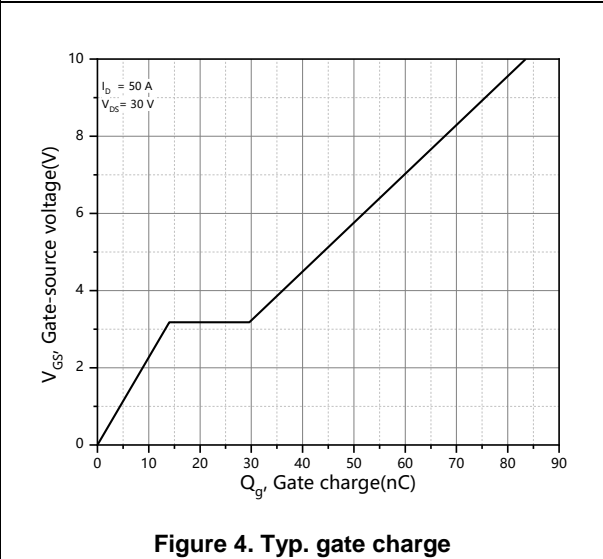


Figure 4. Typ. gate charge

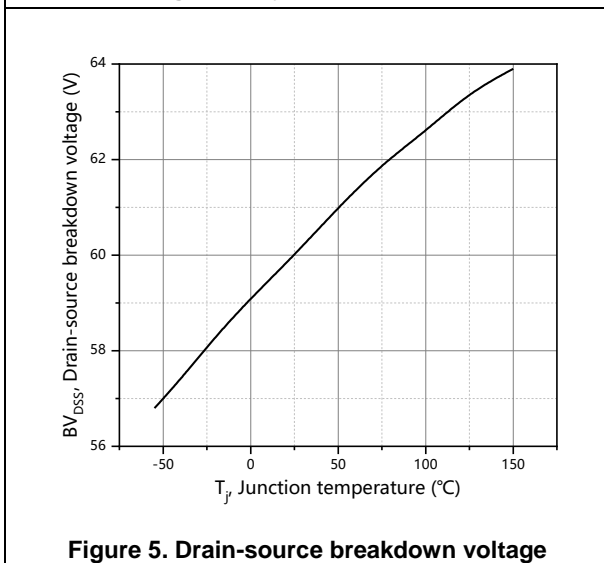


Figure 5. Drain-source breakdown voltage

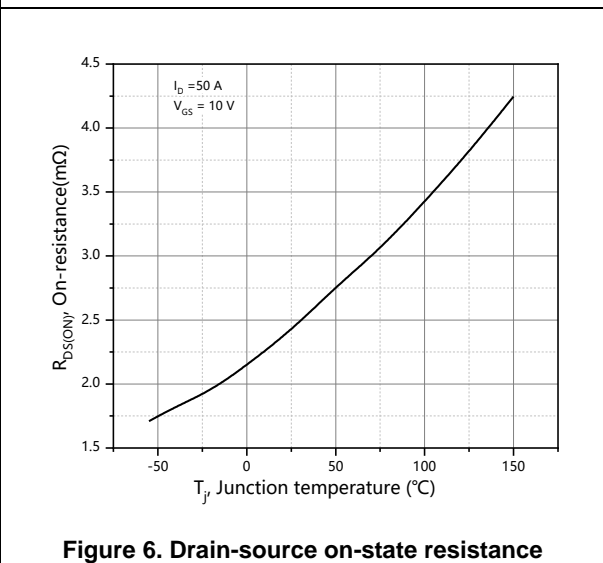
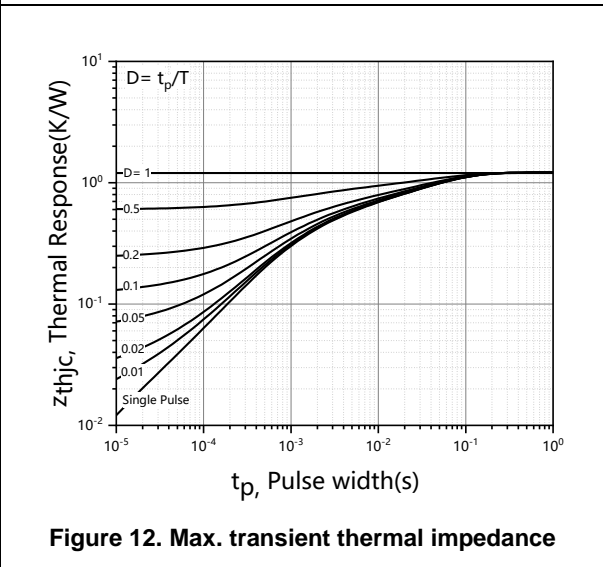
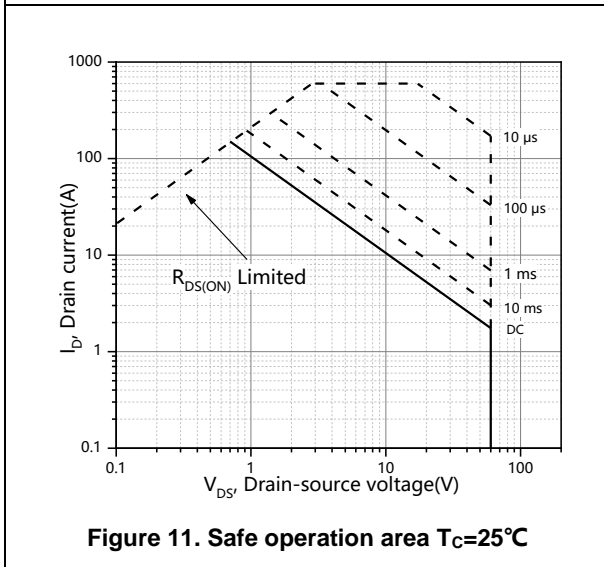
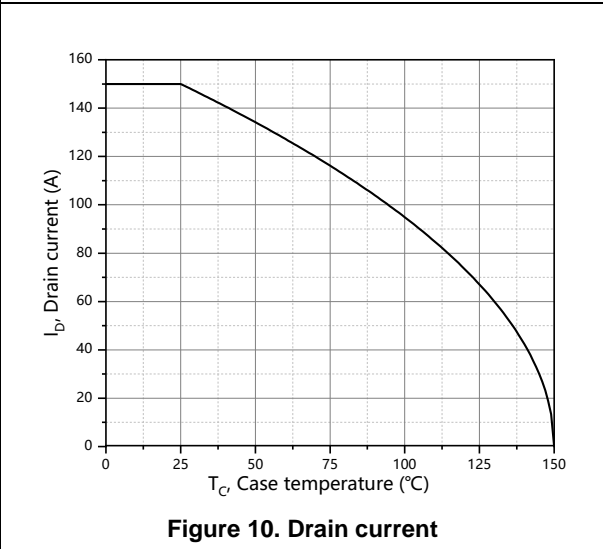
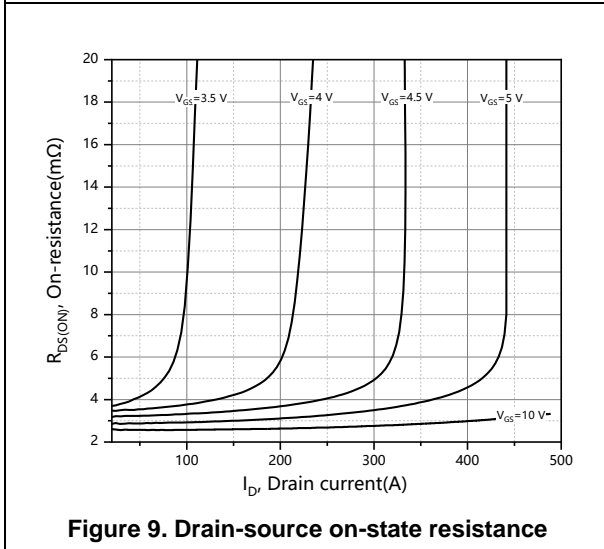
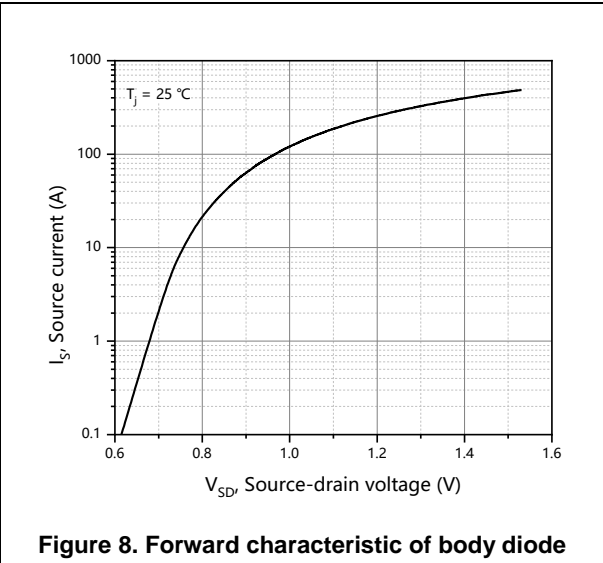
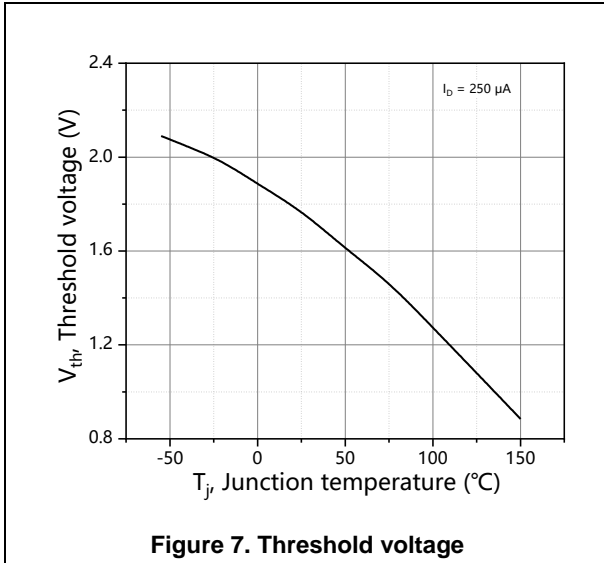


Figure 6. Drain-source on-state resistance



Test circuits and waveforms



Figure 1. Gate charge test circuit & waveform



Figure 2. Switching time test circuit & waveforms

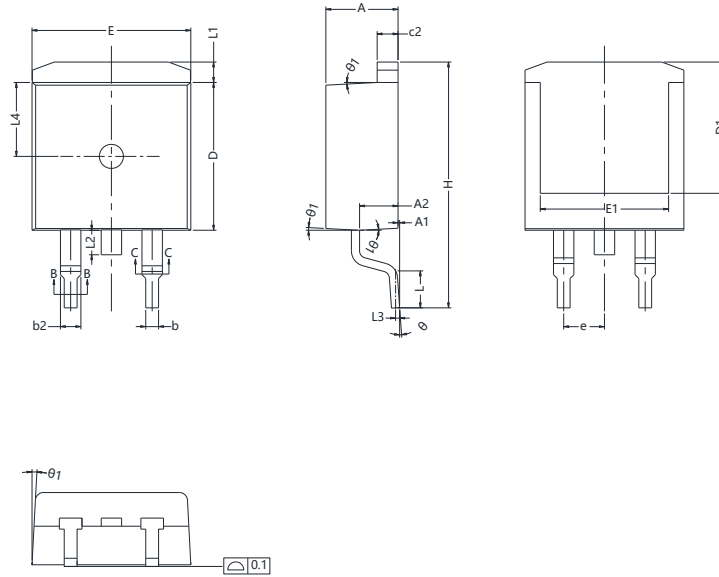


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	0.00	0.10	0.25
A2	2.20	2.40	2.60
b	0.76	-	0.89
b2	1.23	-	1.37
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00	-	-
E	9.80	9.90	10.00
E1	7.80	-	-
e	2.54BSC		
H	14.90	15.30	15.70
L	2.00	2.30	2.30
L1	1.17	1.27	1.40
L2	-	-	1.75
L3	0.25BSC		
L4	4.60 REF		
θ	0°	-	8°
θ1	1°	3°	5°

Version : TO263-J package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO263-J	800	1	800	10	8000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS06R024KF	TO263	yes	yes	yes

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Revision History

Version	Revision History	Date
V1.0	Initial release	2025-07-11