

General Description

FSMOS[®] MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The high V_{th} series is specially designed to use in motor control systems with driving voltage of more than 10V.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

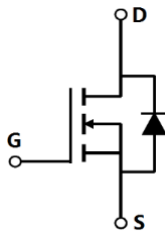
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	150	V
$I_D, pulse$	92	A
$R_{DS(ON), max} @ V_{GS}=10V$	52	m Ω
Q_g	7.1	nC

Marking Information

Product Name	Package	Marking
SFS15R52DNF	TO252	SFS15R52DN

Package & Pin information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	150	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	23	A
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, pulse}$	92	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	23	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S, pulse}$	92	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	61	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	13	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	2.45	$^\circ\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	150			V	$V_{GS}=0\text{ V}, I_D=250\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	3		4.5	V	$V_{DS}=V_{GS}, I_D=250\ \mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		43.4	52	m Ω	$V_{GS}=10\text{ V}, I_D=15\text{ A}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=20\text{ V}$
				-100		$V_{GS}=-20\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=150\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	R_G		2.6		Ω	$f=1\text{ MHz}, \text{Open drain}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		434		pF	$V_{GS}=0\text{ V}$, $V_{DS}=75\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oss}		139		pF	
Reverse transfer capacitance	C_{rss}		3.4		pF	
Turn-on delay time	$t_{d(on)}$		8.5		ns	$V_{GS}=10\text{ V}$, $V_{DS}=75\text{ V}$, $R_G=2\ \Omega$, $I_D=30\text{ A}$
Rise time	t_r		2.2		ns	
Turn-off delay time	$t_{d(off)}$		9.5		ns	
Fall time	t_f		2.8		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		7.1		nC	$V_{GS}=10\text{ V}$, $V_{DS}=75\text{ V}$, $I_D=30\text{ A}$
Gate-source charge	Q_{gs}		3.3		nC	
Gate-drain charge	Q_{gd}		2.2		nC	
Gate plateau voltage	$V_{plateau}$		7.4		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.1	V	$I_S=15\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		51		ns	$V_R=75\text{ V}$, $I_S=30\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		65		nC	
Peak reverse recovery current	I_{rrm}		1.9		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_a=25\text{ }^\circ\text{C}$.
- 5) $V_{DD}=100\text{ V}$, $V_{GS}=10\text{ V}$, $L=0.3\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

Electrical Characteristics Diagrams

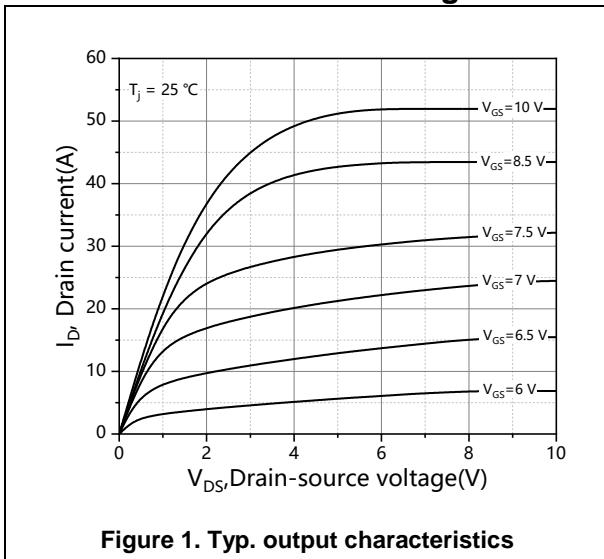


Figure 1. Typ. output characteristics

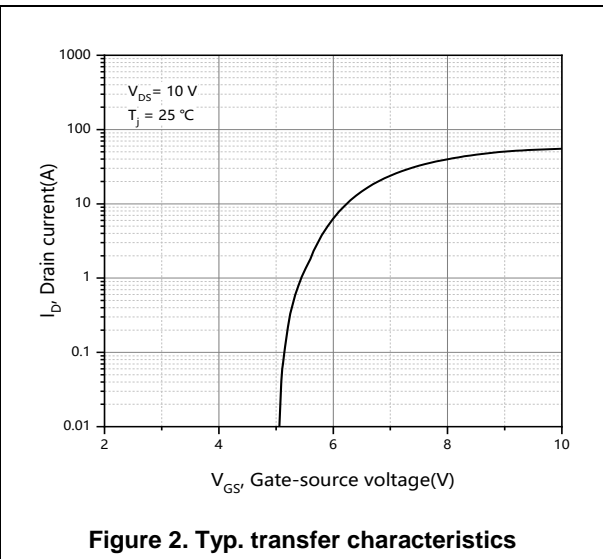


Figure 2. Typ. transfer characteristics

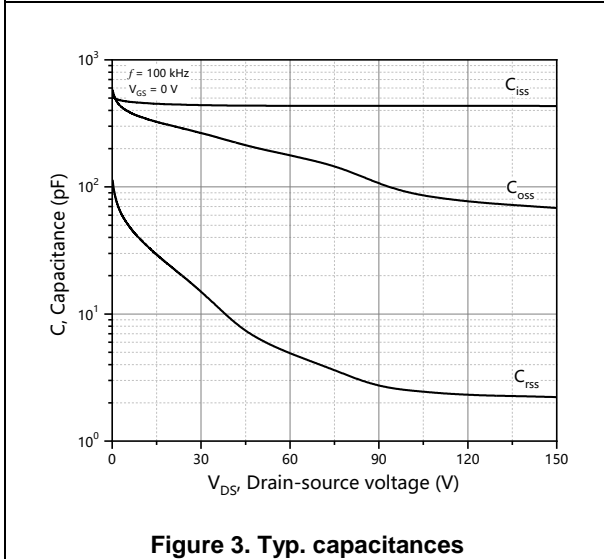


Figure 3. Typ. capacitances

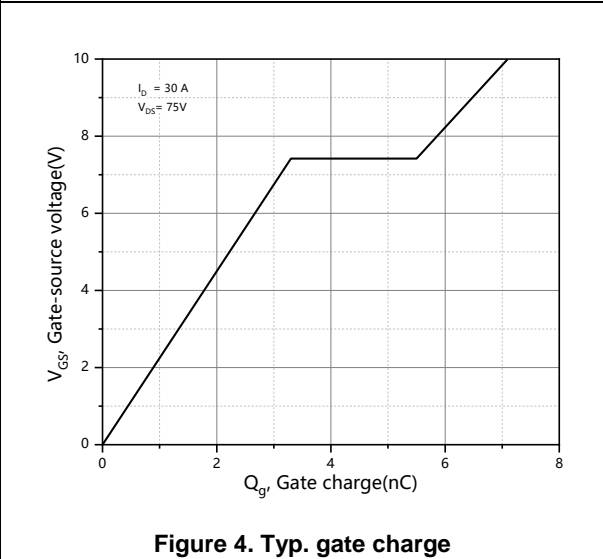


Figure 4. Typ. gate charge

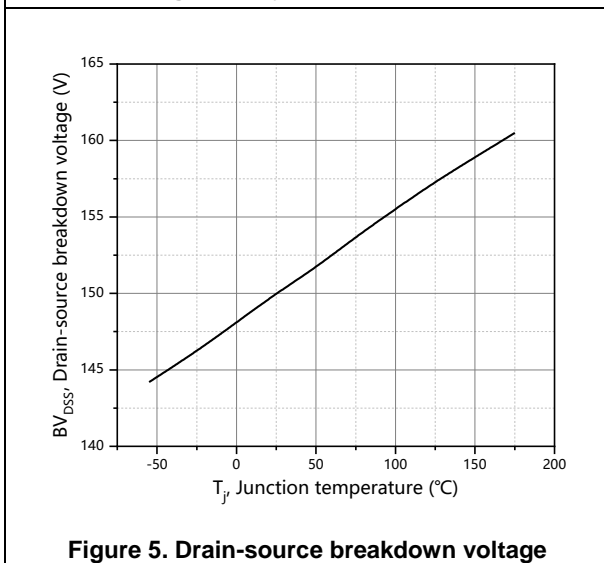


Figure 5. Drain-source breakdown voltage

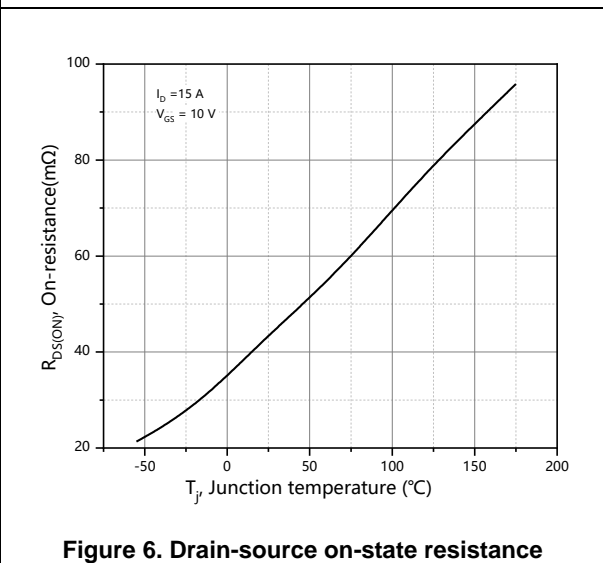
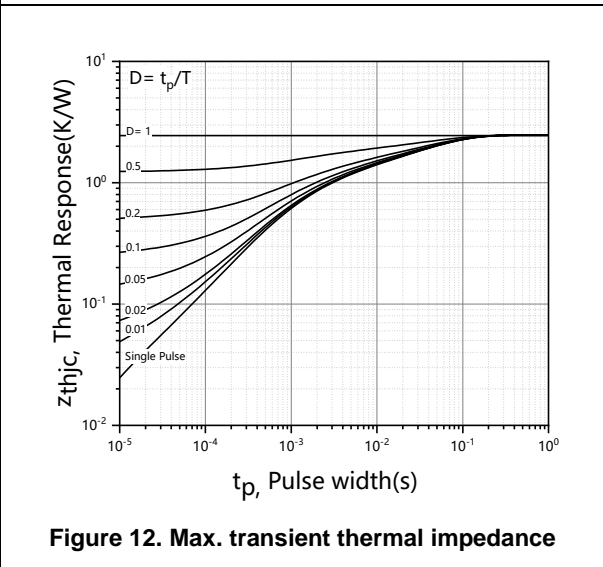
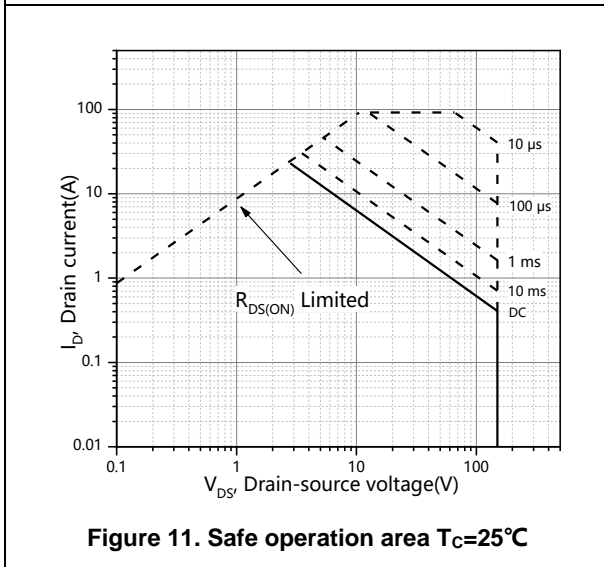
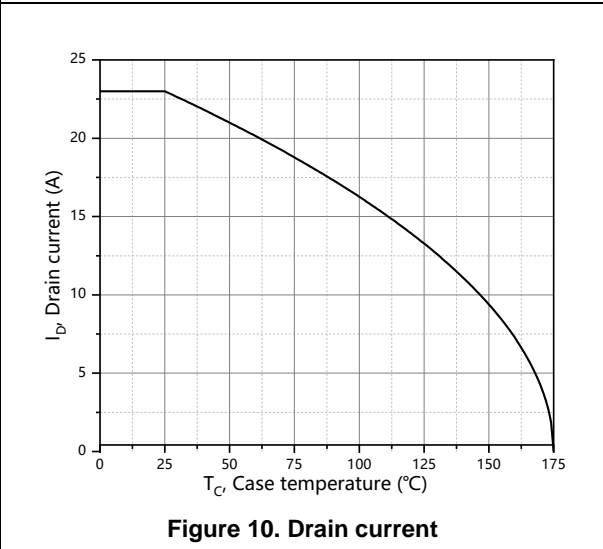
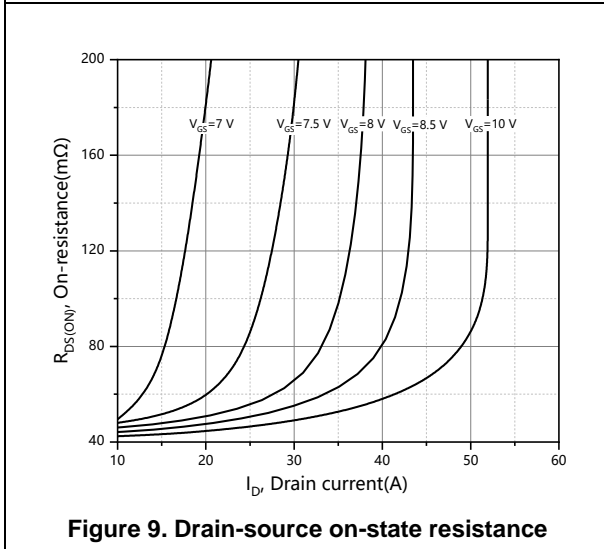
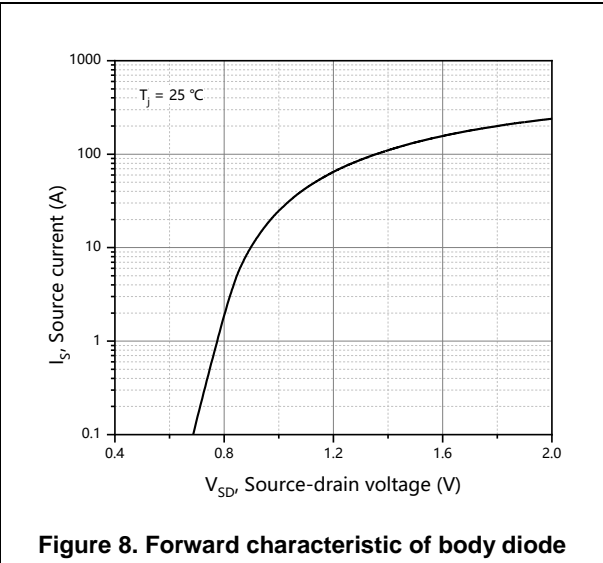
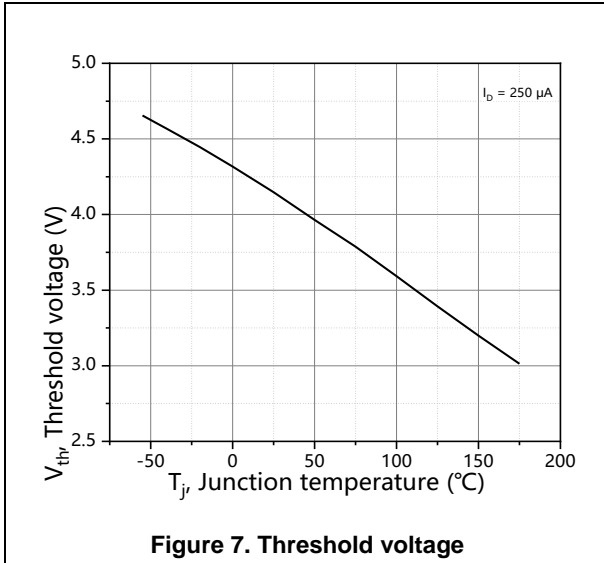


Figure 6. Drain-source on-state resistance



Test circuits and waveforms



Figure 1. Gate charge test circuit & waveform



Figure 2. Switching time test circuit & waveforms

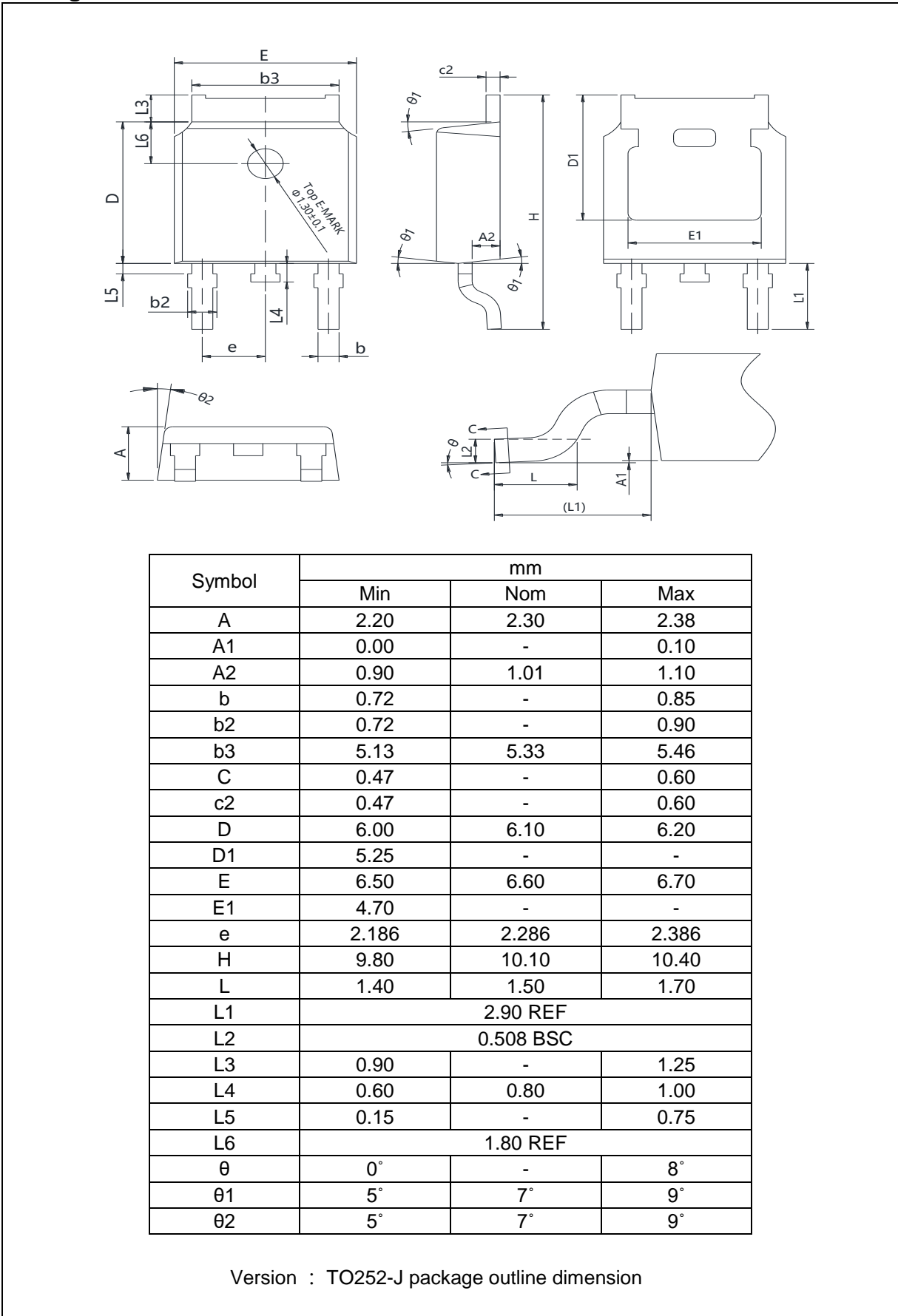


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Ordering Information

Package Type	Units/ Reel	Reels / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-J	2500	2	5000	5	25000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS15R52DNF	TO252	yes	yes	yes

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